



#2/9-6-02
J.R.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of

OSAMU NAKAMURA et al

Serial No. 10/046,893

Filed: 01/17/2002

For: METHOD OF MANUFACTURING

SEMICONDUCTOR DEVICE

)
)
) Group Art Unit: 2812
) Examiner: Unassigned
)
)
)

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INFORMATION DISCLOSURE STATEMENT

Honorable Assistant Commissioner of Patents


Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing references known to Applicant and requests that these references be made of record in the above identified application. Copies of the references listed are submitted herewith in accordance with 37 C.F.R. 1.98(a).

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 50-2280. A duplicate copy of this sheet is attached.

Respectfully submitted,


Eric J. Robinson
Reg. No. 38,285

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on 8-23-02

Robinson Intellectual Property Law Office, P.C.
PMB 955
21010 Southbank Street
Potomac Falls, Virginia 20165
(571) 434-6789

